

METHOD AND SYSTEM FOR SECURING CONTROL-DEVICE-LUN-
MEDIATED ACCESS TO LUNS PROVIDED BY A MASS STORAGE DEVICE

TECHNICAL FIELD

5 The present invention relates to security mechanisms employed within mass storage devices and, in particular, to a method and system for securing access to logical units provided by a mass storage device to remote computers during indirect access of the logical units by the remote computers via a control-device logical unit.

10 BACKGROUND OF THE INVENTION

 The present invention relates to insuring that a remote computer may gain access only to that data stored within the mass storage device for which a remote computer has access privileges. The present invention is described and illustrated with reference to an embodiment included in a disk array controller that services I/O
15 requests from a number of remote computers. However, alternative embodiments of the present invention may be employed in controllers of many other types of storage devices as well as in a general electronic server applications.

 Figure 1 is a block diagram of a standard disk drive. The disk drive 101 receives I/O requests from remote computers via a communications
20 medium 102 such as a computer bus, fibre channel, or other such electronic communications medium. For many types of storage devices, including the disk drive 101 illustrated in Figure 1, the vast majority of I/O requests are either READ or WRITE requests. A READ request requests that the storage device return to the requesting remote computer some requested amount of electronic data stored within
25 the storage device. A WRITE request requests that the storage device store electronic data furnished by the remote computer within the storage device. Thus, as a result of a READ operation carried out by the storage device, data is returned via communications medium 102 to a remote computer, and as a result of a WRITE operation, data is received from a remote computer by the storage device via
30 communications medium 102 and stored within the storage device.

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The disk drive storage device illustrated in Figure 1 includes controller hardware and logic 103 including electronic memory, one or more processors or processing circuits, and controller firmware, and also includes a number of disk platters 104 coated with a magnetic medium for storing electronic data. The disk drive contains many other components not shown in Figure 1, including read/write heads, a high-speed electronic motor, a drive shaft, and other electronic, mechanical, and electromechanical components. The memory within the disk drive includes a request/reply buffer 105 which stores I/O requests received from remote computers and an I/O queue 106 that stores internal I/O commands corresponding to the I/O requests stored within the request/reply buffer 105. Communication between remote computers and the disk drive, translation of I/O requests into internal I/O commands, and management of the I/O queue, among other things, are carried out by the disk drive I/O controller as specified by disk drive I/O controller firmware 107. Translation of internal I/O commands into electromechanical disk operations in which data is stored onto, or retrieved from, the disk platters 104 is carried out by the disk drive I/O controller as specified by disk media read/write management firmware 108. Thus, the disk drive I/O control firmware 107 and the disk media read/write management firmware 108, along with the processors and memory that enable execution of the firmware, compose the disk drive controller.

Individual disk drives, such as the disk drive illustrated in Figure 1, are normally connected to, and used by, a single remote computer, although it has been common to provide dual-ported disk drives for use by two remote computers and multi-port disk drives that can be accessed by numerous remote computers via a communications medium such as a fibre channel. However, the amount of electronic data that can be stored in a single disk drive is limited. In order to provide much larger-capacity electronic data storage devices that can be efficiently accessed by numerous remote computers, disk manufacturers commonly combine many different individual disk drives, such as the disk drive illustrated in Figure 1, into a disk array device, increasing both the storage capacity as well as increasing the capacity for parallel I/O request servicing by concurrent operation of the multiple disk drives contained within the disk array.

Figure 2 is a simple block diagram of a disk array. The disk array 202 includes a number of disk drive devices 203, 204, and 205. In Figure 2, for simplicity of illustration, only three individual disk drives are shown within the disk array, but disk arrays may contain many tens or hundreds of individual disk drives. A disk array contains a disk array controller 206 and cache memory 207. Generally, data retrieved from disk drives in response to READ requests may be stored within the cache memory 207 so that subsequent requests for the same data can be more quickly satisfied by reading the data from the quickly accessible cache memory rather than from the much slower electromechanical disk drives. Various elaborate mechanisms are employed to maintain, within the cache memory 207, data that has the greatest chance of being subsequently re-requested within a reasonable amount of time. The disk array controller 206 may also elect to store data received from remote computers via WRITE requests in cache memory 207 in the event that the data may be subsequently requested via READ requests or in order to defer slower writing of the data to physical storage medium.

Electronic data is stored within a disk array at specific addressable locations. Because a disk array may contain many different individual disk drives, the address space represented by a disk array is immense, generally many thousands of gigabytes. The overall address space is normally partitioned among a number of abstract data storage resources called logical units ("LUNs"). A LUN includes a defined amount of electronic data storage space, mapped to the data storage space of one or more disk drives within the disk array, and may be associated with various logical parameters including access privileges, backup frequencies, and mirror coordination with one or more LUNs. LUNs may also be based on random access memory ("RAM"), mass storage devices other than hard disks, or combinations of memory, hard disks, and/or other types of mass storage devices. Remote computers generally access data within a disk array through one of the many abstract LUNs 208-215 provided by the disk array via internal disk drives 203-205 and the disk array controller 206. Thus, a remote computer may specify a particular unit quantity of data, such as a byte, word, or block, using a bus communications media address corresponding to a disk array, a LUN specifier, normally a 64-bit integer, and a 32-bit,

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automatically executes any writes directed to the first LUN to both the first and second LUNs, so that the second LUN is a faithful mirror copy of the first LUN. Thus, a remote computer, in order to request that the disk array controller mirror a first LUN to second LUN, needs to specify both LUNs in a request for execution of the mirroring operation. After the initial mirror linkage is established, the remote computer can simply write to the first, primary LUN and be assured that the data will be internally copied to the secondary, mirror LUN. As another example, a remote computer may wish to direct a disk array controller to automatically backup a set of LUNs at specified time intervals to a specified backup device.

To reconcile the fact that a number of operations provided to a requesting remote computer by a disk array controller may involve multiple LUNs to the fact that, in general, in invoking any particular operation through many current disk array controller interfaces, a remote computer must specify a single target LUN, a type of virtual LUN known as a control-device LUN ("CDLUN") is provided by disk array controllers as part of the interface through which remote computers invoke operations. CDLUNs are essentially points of access to various operations provided by, and carried out by, a disk array controller. Thus, to specify that a first LUN should be mirrored to a second LUN, a remote computer invokes a mirroring operation and specifies, as the target of the operation, a particular CDLUN. CDLUNs provide indirect memory-mapped access to LUN pair control operations within the array. Control operations directed to specific logical address offsets within the CDLUN are, by definition, directed to the LUN within the array associated with that offset.

A disk array controller may additionally provide, as part of the interface provided to remote computers, various security mechanisms that allow a particular remote computer or group of remote computers to acquire and maintain exclusive access to one or more LUNs. By doing so, a remote computer, or group of remote computers, may shield private data from access, and from potential corruption, by unauthorized entities. Disk arrays were initially developed for use within a single organization, and security to data stored within a disk array was generally obtained by physical isolation of the disk array within a computer room and

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As a first level of security, disk arrays commonly partition access to LUNs via a centrally stored access table managed by the disk array controller. The access table commonly stores entries, each entry comprising an indication of a LUN, an indication of a port, and a unique identifier of a remote computer. When the disk array controller receives a request from a remote computer to carry out an operation with a specified target LUN via a particular communications port, the disk array controller looks in the access table for an entry containing indications of the target LUN, the port through which the request was received, and a unique identifier matching that of the remote computer from which the request was received. If such an entry is found, then the disk array controller allows the operation to proceed. On the other hand, if no such entry is found, then the disk array controller returns an indication that no such storage LUN exists to the requesting remote computer. The access table is normally populated with entries via a console interface by a systems administrator or network manager. Thus, the systems manager or network administrator partitions access to LUNs among remote computers by setting up and maintaining an access table within the disk array that is used by the disk array controller to check each incoming request for execution of an operation by the disk array controller for proper authorization.

Unfortunately, the class of operations which span multiple LUNs, and which require specification of a target CDLUN, as described above, are currently not adequately secured by the access table method described above. Currently, the disk array controller checks a requested operation that specifies a target CDLUN by checking whether an entry in the access table includes a unique identifier of the requesting remote computer, an indication of the CDLUN, and an indication of the port through which the request was received. However, the disk array controller does

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In one embodiment of the present invention, a disk array controller uses two access tables in order to check for authorization of an operation requested by

For the many non-CDLUN-mediated operations, such as common read and write operations, authorization checking by the disk array controller is unchanged. For such an operation to proceed, the disk array controller must find a corresponding entry in the access table. The disk array controller employs the two-level access table and supplemental access table authorization check only for requests for operations that specify a target CDLUN and that include specifications of additional LUNs, such as a request for LUN mirroring.

Thus, in order to successfully request an operation that specifies a target CDLUN and that includes specification of additional LUNs, a requesting remote computer must be authorized to access the specified target CDLUN, and the target CDLUN must be authorized to access each additionally specified LUN. Both the access table and the supplemental access table are populated, organized, and

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Figure 1 is a block diagram of a standard disk drive.

15 DETAILED DESCRIPTION OF THE INVENTION

The present invention is related to security measures undertaken by a disk array controller that checks authorization of a requesting remote computer for requested operations. In certain current firmware implementations of disk array controllers, as explained previously, the disk array controller searches an access table for an entry that includes an indication of the target LUN specified as part of the request, an indication of the port through which the request was received, and a unique identifier of the requesting remote computer. When such an entry is found in the access table, the disk array controller continues to carry out the requested operation, but when no such entry is found, the disk array controller returns an indication to the requesting remote computer that no such storage LUN exists. As previously discussed, this method of authorization is inadequate for a class of operations provided by the disk array controller directed to a specified target CDLUN and involving additional specified LUNs. As previously discussed, operations that request mirroring of a first LUN to a second LUN, or of a first group of LUNs to a second group of LUNs, or operations that request a group of LUNs be automatically backed up to a specified backup device, are examples of operations that include a

5 A mistaken or malicious remote computer can easily gain access to LUNs to which the mistaken or malicious remote computer does not have access privileges via a target CDLUN for which the mistaken or malicious remote computer does have access privileges.

As will be discussed in detail, below, the described embodiment of the present invention is economically and relatively easily implemented within the firmware implementation of a disk array controller. First, the authorization checking methodology for the bulk of operations provided by the disk array controller that include target LUNs rather than target CDLUNs is unchanged. The changes to the current methodology for authorization checking relate only to that class of operations that are directed to a specified target CDLUN and that involve additional specified LUNs. For that class of operation, a two-part authorization is used, first part identical to the current authorization methodology, and implementation of the second part quite similar to the current authorization methodology.

One embodiment of the present invention is discussed in detail, below, with reference to a C++-like pseudocode implementation. A C++-like pseudocode implementation is chosen for clarity and for illustrative purposes. In general, disk array controllers are implemented in firmware or directly in logic circuits. Because
 5 firmware implementations as well as logic circuit implementations are, by modern techniques, designed and implemented by compilation of high-level program-like specifications, it is entirely appropriate to illustrate implementation of the present invention in terms of pseudocode.

The following C++-like pseudocode implementation illustrates the
 10 current security mechanism used within certain disk array controllers as well as one embodiment of the current invention. First, several constants and three class declarations representing identifiers for LUNs, ports, and servers are provided below, a server in the pseudocode implementation equivalent to the more general notion of a remote computer:

```

15
    1  const int WWW_name_length = 20;
    2  const int TableLength = 1000;

    3  class LUN
20  4  {
    5  private:
    6      int name;
    7  public:
    8      int getVal()                {return name;};
25  9      LUN& operator=(int l)      {name = l; return *this;};
10 10     LUN& operator=(LUN& l)      {name = l.getVal(); return *this;};
11 11     bool operator==(LUN& l)     {return (l.getVal() == name);};
12 12     bool operator<(LUN& l)      {return (name < l.getVal());};
13 13     bool operator>(LUN& l)      {return (name > l.getVal());};
30 14     LUN(LUN& l)                  {name = l.getVal();};
15 15     LUN (int l)                  {name = l;};
16 16     LUN();
17 17 };

35 18 class port
19 {
20 private:
21     int name;
22 public:
40 23     int getVal()                {return name;};
24     port& operator=(int p)       {name = p; return *this;};

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Next, two class declarations that define an access table are provided:

The class "AccessTable," declared above on lines 21-32, represents an access table used by an implementation of a disk array controller for storing authorizations for access by particular servers via particular ports to particular LUNs and CDLUNs. An instance of the class "AccessTable" includes an array of instances of the class "AccessEntry," essentially representing LUN/port/server triples. The array of instances of the class "AccessEntry," called "table," is declared above on line 4, and, on line 5, an integer data member "size" is declared that contains the number of valid entries currently stored within the access table represented by an instance of the class "AccessTable." Note that, in the current implementation, instances of the class "AccessEntry" are stored sequentially in the table starting with the first slot within the table having index "0." Thus, a table with two valid entries will include instances of the class "AccessEntry" in slots of the table having indices 0 and 1, and the data member "size" will have the value 2. Thus, size represents both the number of valid entries within the table and the index of the next free slot within

Next, implementations of various member functions of classes "LUN," "port," and "server" are provided. These implementations are quite straightforward, although, in many cases, far from optimally efficient. In the development of control logic and program code, there are commonly many tradeoffs between run-time efficiency, and design and implementation efficiencies, run-time efficiency and code complexity, and run-time efficiency versus economics of maintenance and subsequent enhancement. For illustrative purposes, we have chosen simple algorithms, but not necessarily optimally efficient algorithms, favoring simplicity over run-time efficiency, although many other tradeoffs and corresponding implementations may be chosen, and are within the scope of the present invention. Many of the member function implementations in this section and in a following section are straightforward, and will not be discussed at length.

```

25  1  LUN::LUN()
    2  {
    3  }

    4  port::port()
30  5
    6  }

    7  server::server()
    8  {
35  9  }

```

```

10 server& server::operator=(char* s)
11 {
5 12     char* k = name;
13     for (int i = 0; i < WWW_name_length; i++) *k++ = *s++;
14     return *this;
15 }

10 16 server& server::operator=(server& s)
17 {
18     char* s1 = s.getVal();
19     char* k = name;
20     for (int i = 0; i < WWW_name_length; i++) *k++ = *s1++;
15 21     return *this;
22 }

23 bool server::operator==(server& s)
24 {
20 25     char* s1 = s.getVal();
26     char* k = name;
27     for (int i = 0; i < WWW_name_length; i++)
28     {
29         if (*k++ != *s1++) return false;
25 30     }
31     return true;
32 }

33 bool server::operator<(server& s)
30 34 {
35     char* s1 = s.getVal();
36     char* k = name;
37     for (int i = 0; i < WWW_name_length; i++)
38     {
35 39         if (*k < *s1) return true;
40         else if (*k++ > *s1++) return false;
41     }
42     return false;
43 }

40 44 bool server::operator>(server& s)
45 {
46     char* s1 = s.getVal();
47     char* k = name;
45 48     for (int i = 0; i < WWW_name_length; i++)
49     {
50         if (*k > *s1) return true;
51         else if (*k++ < *s1++) return false;
52     }
50 53     return false;
54 }

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55 server::server(server& s)
56 {
57     char* s1 = s.getVal();
58     char* k = name;
5 59     for (int i = 0; i < WWW_name_length; i++) *k++ = *s1++;
60 }

61 server::server (char* s)
62 {
10 63     char* k = name;
64     for (int i = 0; i < WWW_name_length; i++) *k++ = *s++;
65 }

```

The implementation of the server assignment operator, shown above on lines 10-15, typifies implementation of an assignment operator. In this case, the value stored in the data member "name" is assigned to have the value represented by a character string passed to the assignment function as argument "s." In the *for*-loop of line 13, the character string referenced by argument "s" is copied to the data member "name," referenced by the local variable "k." Finally, the assignment function returns a reference to the current instance of the class "server" on line 14.

The implementation of the server "<" relational operator, shown above on lines 33-43 typifies implementation of a relational operator. This operator compares the value of a current instance of the class "server" to that of a different server instance referenced by reference argument "s." If the value of the current instance of class "server" is less than that of the server instance referenced by argument "s," then the "<" operator returns a true Boolean value, and otherwise returns a false Boolean value. In the *for*-loop of lines 37-41, the value of the data member "name" of the current instance is compared to that of the server instance referenced by argument "s." The implemented comparison is equivalent to the classical lexical string comparison that produces the familiar ordering of names in a telephone book.

Next, implementations of various member functions of the class "AccessEntry" are provided:

```

35 1 AccessEntry& AccessEntry::operator=(AccessEntry& ae)
   2 {
   3     ln = ae.getLUN();

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4      pt = ae.getPort();
5      sv = ae.getServer();
6      return *this;
7  }

5
8  bool AccessEntry::operator==(AccessEntry& ae)
9  {
10     return (ae.getLUN() == ln &&
11             ae.getPort() == pt &&
10    12             ae.getServer() == sv);
13 }

14 bool AccessEntry::operator<(AccessEntry& ae)
15 {
15    16     if (ln < ae.getLUN()) return true;
17     else if (ln == ae.getLUN())
18     {
19         if (pt < ae.getPort()) return true;
20         else if (pt == ae.getPort())
20    21         {
22             if (sv < ae.getServer()) return true;
23             else return false;
24         }
25         else return false;
25    26     }
27     else return false;
28 }

29 bool AccessEntry::operator>(AccessEntry& ae)
30 {
30    31     if (ln > ae.getLUN()) return true;
32     else if (ln == ae.getLUN())
33     {
34         if (pt > ae.getPort()) return true;
35         else if (pt == ae.getPort())
35    36         {
37             if (sv > ae.getServer()) return true;
38             else return false;
39         }
40         else return false;
40    41     }
42     else return false;
43 }

45    44 AccessEntry::AccessEntry(LUN& l, port& p, server& s)
45    46 {
46    47     ln = l;
47    48     pt = p;
48    49     sv = s;
50    49 }

50 AccessEntry::AccessEntry()

```

51 {
52 }

Next, implementations of various member functions of the class

5 "AccessTable" are provided below:

```

1  int AccessTable::addEntry(LUN& l, port& p, server& s)
2  {
3      int i = 0;
10 4      int j;
5      AccessEntry ae(l, p, s);

6      if (size == TableLength) return -1;
7      while (i < size && table[i] < ae) i++;
15 8      if (i < size && table[i] == ae) return -2;
9      else
10     {
11         for (j = size; j > i; j--) table[j] = table[j-1];
12         table[i] = ae;
20 13         size++;
14     }
15     return size;
16 }

25 17 int AccessTable::deleteEntry(LUN& l, port& p, server& s)
18 {
19     int i, j;

20     i = findEntry(l, p, s);
30 21     if (i >= 0)
22     {
23         j = i+1;
24         while (j < size) table[j++] = table[j-1];
25         size--;
35 26         return size;
27     }
28     return -1;
29 }

40 30 int AccessTable::findEntry(LUN& l, port& p, server& s)
31 {
32     int i = 0;
33     AccessEntry ae(l, p, s);

45 34     while (i < size && table[i] < ae) i++;
35     if (i < size && table[i] == ae) return i;
36     else return -1;
37 }

```

```

38 bool AccessTable::retrieveEntry(int index, LUN& l, port& p, server& s)
39 {
40     if (index >= 0 && index < size)
41     {
5 42         l = table[index].getLUN();
43         p = table[index].getPort();
44         s = table[index].getServer();
45         return true;
46     }
10 47 else return false;
48 }

49 AccessTable::AccessTable()
50 {
15 51     size = 0;
52 }

```

Again, implementations of the AccessTable member functions are straightforward and only the implementation of AccessTable member function "addEntry" will be discussed as a representative example. Member function "addEntry" receives reference arguments that reference a LUN, port, and server that are to be added to the access table as a triple represented by an instance of the class AccessTable. On line 5, the local variable "ae" is constructed to contain the LUN/port/server triple specified by the reference arguments. If the access table is full, as detected by addEntry on line 6, then no entry is added and a negative value is returned. In the *while*-loop of line 7, addEntry scans the valid entries within the table for an instance of the class "AccessEntry" with a value greater than or equal to that of local variable "ae," as defined by the AccessEntry relational operator "<." At the conclusion of the *while*-loop, the local variable "i" is either the offset of the first valid entry greater than or equal to the value of local variable "ae" or the offset of the next available entry within the table. If an entry exists in the table and represents the same triple as represented by local variable "ae," as detected by addEntry on line 8, then addEntry returns a negative value, since there is no point adding a second equivalent triple to the access table. Otherwise, in the *for*-loop on line 12, addEntry moves all entries that will follow the entry to be added downward by one place in the table to make space for the new entry, and adds the new entry on line 13. Following addition

With the declarations and implementations provided above, the following function "currentAuthorization" is provided to illustrate a current authorization technique employed in certain currently available disk array controllers:

15 The function "currentAuthorization" may be called by code within the disk array controller to check whether a requested operation is authorized, in this case, an operation against a target CDLUN that includes specification of additional LUNs, such as a mirroring or backup request discussed earlier. The function "currentAuthorization" receives reference arguments that refer to a LUN, port, server, and access table, "CDLUN," "p," "s," and "at," respectively. The function currentAuthorization, on line 4, calls the access table member function "findEntry" to determine whether the access table currently includes an entry representing the LUN/port/server triple CDLUN/p/s. If so, then function "currentAuthorization" returns true, on line 5, and otherwise returns false on line 6. Thus, as discussed earlier, the current authorization technique involves checking whether the target CDLUN, port through which the request was received, and unique identifier of the requesting server, or remote computer, occurs as a triple in the authorization table, and, if so, considers the requested operation to be authorized. However, as discussed above, this authorization technique is quite deficient, because the authorized operation may involve accessing LUNs which the requesting server does not have authorization to access, leading to potential corruption of data or reading of data belonging to an organization external to that of the requesting server.

One embodiment of the current invention employs the above declarations and implementations of the LUN, port, server, accessEntry and accessTable classes, along with two additional classes provided below:

```

5   1  class SupplementalAccessEntry
    2  {
    3  private:
    4      LUN ln;
    5      LUN cd;
10   6  public:
    7      LUN&  getLUN()          {return ln;};
    8      void  setLUN(LUN& l)    {ln = l;};
    9      LUN&  getCDLUN()        {return cd;};
   10     void  setCDLUN(LUN& l)   {cd = l;};
   15  11     SupplementalAccessEntry& operator=(SupplementalAccessEntry& ae);
   12     bool  operator==(SupplementalAccessEntry& ae);
   13     bool  operator<(SupplementalAccessEntry& ae);
   14     bool  operator>(SupplementalAccessEntry& ae);
   15     SupplementalAccessEntry(LUN& l, LUN& c);
20  16     SupplementalAccessEntry();
   17 };

   18 class SupplementalAccessTable
   19 {
25  20 private:
   21     SupplementalAccessEntry table[TableLength];
   22     int size;
   23 public:
   24     int  addEntry(LUN& l, LUN& c);
30  25     int  deleteEntry(LUN& l, LUN& c);
   26     int  findEntry(LUN& l, LUN& c);
   27     bool  retrieveEntry(int index, LUN& l, LUN& c);
   28     SupplementalAccessTable();
   29 };
35

```

The class "SupplementalAccessEntry" is analogous to the previously described class "AccessEntry," and the class "SupplementalAccessTable" is analogous to the previously described class "AccessTable." A SupplementalAccessEntry presents a CDLUN/LUN pair and a SupplementalAccessTable includes a number of CDLUN/LUN pairs. As discussed earlier, the presence of a particular CDLUN/LUN pair indicates that the CDLUN may access the LUN as part of an operation for which the CDLUN is the target CDLUN. The two new classes are quite similar to the previously declared classes

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```

5 1 SupplementalAccessEntry&
2 SupplementalAccessEntry::operator=(SupplementalAccessEntry& ae)
3 {
4     ln = ae.getLUN();
5     cd = ae.getCDLUN();
10 6     return *this;
7 }

8 bool SupplementalAccessEntry::operator==(SupplementalAccessEntry& ae)
9 {
15 10     return (ln == ae.getLUN() && cd == ae.getCDLUN());
11 }

12 bool SupplementalAccessEntry::operator<(SupplementalAccessEntry& ae)
13 {
20 14     if (ln < ae.getLUN()) return true;
15     else if (ln == ae.getLUN())
16     {
17         if (cd < ae.getCDLUN()) return true;
18         else return false;
25 19     }
20     else return false;
21 }

22 bool SupplementalAccessEntry::operator>(SupplementalAccessEntry& ae)
30 23 {
24     if (ln > ae.getLUN()) return true;
25     else if (ln == ae.getLUN())
26     {
27         if (cd > ae.getCDLUN()) return true;
35 28         else return false;
29     }
30     else return false;
31 }

40 32 SupplementalAccessEntry::SupplementalAccessEntry(LUN& l, LUN& c)
33 {
34     ln = l;
35     cd = c;
36 }

45

37 SupplementalAccessEntry::SupplementalAccessEntry()
38 {
39 }

```

```

40 int SupplementalAccessTable::addEntry(LUN& l, LUN& c)
41 {
5 42     int i = 0;
43     int j;
44     SupplementalAccessEntry ae(l, c);

45     if (size == TableLength) return -1;
10 46     while (i < size && table[i] < ae) i++;
47     if (i < size && table[i] == ae) return -2;
48     else
49     {
50         for (j = size; j > i; j--) table[j] = table[j-1];
15 51         table[i] = ae;
52         size++;
53     }
54     return size;
55 }

20 56 int SupplementalAccessTable::deleteEntry(LUN& l, LUN& c)
57 {
58     int i, j;

25 59     i = findEntry(l, c);
60     if (i >= 0)
61     {
62         j = i+1;
63         while (j < size) table[i++] = table[j++];
30 64         size--;
65         return size;
66     }
67     else return -1;
68 }

35 69 int SupplementalAccessTable::findEntry(LUN& l, LUN& c)
70 {
71     int i = 0;
72     SupplementalAccessEntry ae(l, c);

40 73     while (i < size && table[i] < ae) i++;
74     if (i < size && table[i] == ae) return i;
75     else return -1;
76 }

45 77 bool SupplementalAccessTable::retrieveEntry(int index, LUN& l, LUN& c)
78 {
79     if (index >= 0 && index < size)
80     {
50 81         l = table[index].getLUN();
82         c = table[index].getCDLUN();
83         return true;

```



```
5 87 SupplementalAccessTable::SupplementalAccessTable()
88 {
89     size = 0;
90 }
```

```

15 1  boolnewAuthorization (LUN& CDLUN, LUN* LUNlist, int listSize,
2      port& p, server& s, AccessTable& at,
3      SupplementalAccessTable& st)
4  {
5      if (at.findEntry(CDLUN, p, s) >= 0)
20 6      {
7          while (listSize > 0)
8          {
9              if (st.findEntry(CDLUN, *LUNlist) < 0) return false;
10             listSize--;
25 11             LUNlist++;
12         }
13         return true;
14     }
15     else return false;
30 16 }

```

This new authorization function receives the following arguments: (1) "CDLUN," a reference to a LUN that represents a target CDLUN of an operation; (2) "LUNlist," a pointer to a list of LUNs also included in the operation, such as LUNs to be mirrored in a mirroring operation; (3) "listSize," an integer specifying the number of LUNs in the list "LUNlist;" (4) "p," the port through which the request for operation was received by the disk array controller (5) "s," the server, or remote computer, from which the request was received; (6) "at," a reference to an access table; and (7) "st," a reference to a supplemental access table. First, on line 5, newAuthorization determines whether the triple CDLUN/p/s currently occurs within the access table, just as in line 4 of the previous authorization technique embodied in

Thus, the above-described embodiment of the present invention adds a second access table, the supplemental access table, to the firmware implementation of the disk array controller, providing the disk array controller with the ability to conduct a more thorough authorization check for requests by remote computers for operations against target CDLUNs that include specification of additional LUNs. The new authorization technique involves checking for authorization of the requesting remote computer requests an operation against the specified target LUN, as well as checking that the specified target CDLUN is authorized to access the additionally specified LUNs of the request. By using a two-tiered authorization mechanism, the described embodiment of the present invention closes a significant security hole that formerly existed in disk array controller implementations and in the implementations of controllers of many other types of mass storage devices. The authorized entities in the above-described embodiment are remote computers, but other entities such as remote processes or users may be authorized in alternative embodiments.

Although the present invention has been described in terms of a particular embodiment, it is not intended that the invention be limited to this embodiment. Modifications within the spirit of the invention will be apparent to those skilled in the art. For example, the present invention may be employed in implementations of controllers for a wide variety of mass storage devices remotely

The foregoing description, for purposes of explanation, used specific nomenclature to provide a thorough understanding of the invention. However, it will be apparent to one skilled in the art that the specific details are not required in order to practice the invention. The foregoing descriptions of specific embodiments of the present invention are presented for purpose of illustration and description. They are not intended to be exhaustive or to limit the invention to the precise forms disclosed. Obviously, many modifications and variations are possible in view of the above teachings. The embodiments are shown and described in order to best explain the principles of the invention and its practical applications, to thereby enable others skilled in the art to best utilize the invention and various embodiments with various modifications as are suited to the particular use contemplated. It is intended that the scope of the invention be defined by the following claims and their equivalents: